

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of

Hiroshi KANNO

Serial No.: 10/736,627

Filed: December 17, 2003

For: TRANSMISSION LINE AND SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE



: Customer Number: 20277  
:  
: Confirmation Number: Not yet assigned  
:  
: Group Art Unit: Not yet assigned  
:  
: Examiner: Not yet assigned  
:

**REQUEST FOR CORRECTED FILING RECEIPT**

Mail Stop OFR  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The above-referenced patent application was filed in the U.S. Patent and Trademark Office on December 17, 2003, as evidenced by the attached copies of the return receipt postcard and Transmittal Letter. To date, the undersigned has not received an official Filing Receipt regarding this application.

It is respectfully requested that the undersigned be furnished with an official Filing Receipt as soon as possible.

Respectfully submitted,

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**Date: June 17, 2004**